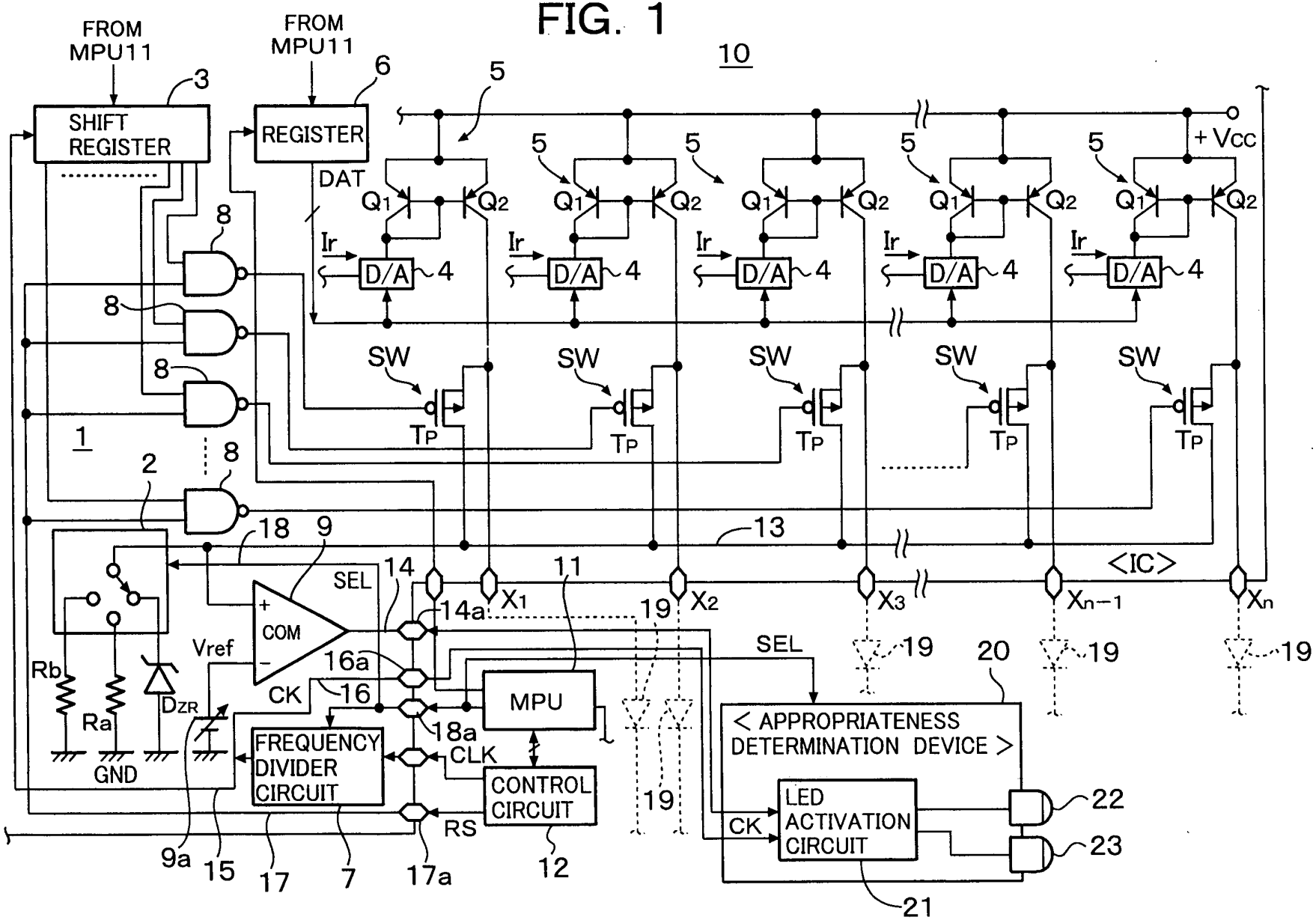
FIG. 1


FROM MPU11
FROM MPU11
10
SHIFT REGISTER
REGISTER
DAT
+ Vcc
Q1
Q2
Ir
D/A
SW
TP
<IC>
X1
X2
X3
Xn−1
Xn
SEL
COM
Vref
Rb
Ra
DZR
GND
CK
MPU
CLK
RS
FREQUENCY DIVIDER CIRCUIT
CONTROL CIRCUIT
< APPROPRIATENESS DETERMINATION DEVICE >
LED ACTIVATION CIRCUIT